

Atty Docket No. 2101239-991021

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**HETEROSTRUCTURE BIPOLAR TRANSISTOR POWER AMPLIFIER MODULE
WITH DYNAMIC VOLTAGE SUPPLY FOR IMPROVED EFFICIENCY**

the specification of which (check one) ☒ is attached hereto or ☐ was filed on _____ as
Application No. _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed
Yes No

Number Country Day/Month/Year Filed

Number Country Day/Month/Year Filed

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) below.

60/456,423 March 19, 2003
Application Number Filing Date

Application Number Filing Date

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information

Gray Cary/F: M7161406.1

1

which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Filing Date	Status: Patented, Pending, Abandoned
--------------------	-------------	--------------------------------------

Application Number	Filing Date	Status: Patented, Pending, Abandoned
--------------------	-------------	--------------------------------------

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of inventor Aizon B. CANILAO

Inventor's signature



3/17/04

Date

Residence 33 Union Square # 423, Union City, CA 94587

Citizenship Philippines

Post Office Address 33 Union Square # 423, Union City, CA 94587

Full name of inventor Ronald P. GREEN

Inventor's signature



3/17/04

Date

Residence 2363 Shoreside Ct., Santa Clara, CA 95054

Citizenship Canada

Post Office Address same as above

Full name of inventor Gi Young LEE

Inventor's signature



3/17/04

Date

Residence 12741 Carniel Ave, Saratoga, CA 95070

Citizenship Republic of Korea

Post Office Address same as above

Attorney Docket No.: 2101239-991021

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Canilao, et al. Group Art Unit: Not Yet Assigned
Serial No.: Not Yet Assigned Examiner: Not Yet Assigned
Filed: Herewith
Title: **HETEROSTRUCTURE BIPOLAR TRANSISTOR POWER AMPLIFIER
MODULE WITH DYNAMIC VOLTAGE SUPPLY FOR IMPROVED
EFFICIENCY**

* * *

POWER OF ATTORNEY BY ASSIGNEE

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned, being Assignee of the entire right, title and interest in the above-identified application by virtue of an Assignment, a copy thereof is attached, under 37 C.F.R. §3.71, to prosecute the application to the exclusion of the Inventor(s), and grants Power of Attorney and correspondence address as stated herein.

DYNALINEAR TECHNOLOGIES, INC., hereby appoints:

Practitioners at Customer Number: 26379

Gray (ary)M\7161408.1
2101239-991021

- 1 -

Attorney Docket No.: 2101239-991021

as attorneys/agents with full power of substitution and revocation to prosecute this application
and to transact all business in the Patent and Trademark Office connected therewith.

Assignee:

Dynalinear Technologies, Inc.

Date: March 17, 2004Printed Name: Gi Young LeeTitle: President